





## **Certificate of Presentation**

We hereby certify that the work

Procedure to use diamond layout style and reduce analogue IC area

## by

José Roberto Banin , Salvador Pinillos Gimenez, Rodrigo Alves de Lima Moreto and G. A. da Silva

was presented at the XIV Workshop on Semiconductors and Micro & Nano Technology, in Campinas-SP, held on April 11-12, 2019.

Dr. Jacobus Swart SEMINATEC 2019 General Chair