



# Certificate of Presentation

We hereby certify that the work

**Radiation Hardened by Design library and Triple Module Redundancy with Error Correction Codes for development Space Wire ASIC.**

by

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was presented at the XIV Workshop on Semiconductors and Micro & Nano Technology, in Campinas-SP, held on April 11-12, 2019.

A handwritten signature in blue ink, appearing to read "Jacobus Swart", written over a horizontal line.

Dr. Jacobus Swart  
SEMINATEC 2019 General Chair